

~~CONFIDENTIAL~~

1/29/6
view
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group 222
Examiner R. A. Farley

Amendment

A
L Washington
1-29-81

Arent H. Kits Van Heyningen

Dual Channel Delay Sensing Circuit (U)

JAN 20 1981

Serial No. 944,530 ✓

GROUP 220

Filed September 11, 1978

RAYTHEON COMPANY
EXECUTIVE OFFICES
141 SPRING STREET
LEXINGTON, MA 02173
DATE 28 Dec 1979

RECEIVED

Hon. Commissioner of Patents and Trademarks

DEC 31 1979
On

Washington, D.C. 20231

GROUP 220
LICENSING & REVIEW

Sir:

AMENDMENT

(U) In response to the Office Action dated October 30, 1979,
please amend herein as follows:

In the Claims

- 1 1. (Amended) A delay measurement circuit comprising:
2 (C) first and second channels for receiving first and second
3 signals, each of said signals having a leading edge and a
4 trailing edge;
5 a leading edge circuit coupled to said first and said
6 second channels for signaling the temporal relationship of
7 the leading edges of said first and said second signals;
8 a trailing edge circuit coupled to said first and said
9 second channels for signaling the temporal relationship of
10 the trailing edges of said first and said second signals;
11 a memory coupled to said leading edge circuit for storing
12 data of said leading edges;
13 timing means activated by said leading edge circuit for
14 measuring a delay between leading edges of said first and said
15 second signals; [and]

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|---------------|------------------|-----------|
| CLASSIFIED BY | NAV SEH INST C54 | 11.5 Oct. |
| DECLASSIFY ON | NA | 1974 |
| REVIEW ON | NA | |

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